

ABSTRACT OF THE DISCLOSURE

A surveillance camera system includes a multiplexer. A camera switch circuit provided on the multiplexer selects each of a plurality of surveillance cameras in a time-division manner. An image processing circuit included in the multiplexer fetches a
5 photographed image signal output from a selected surveillance camera, and adds VBI information having a recording bit of “1” to the photographed image signal at a timing according to a recording mode setting. An HDR recodes the photographed image signal in a hard disk when the recording bit included in the VBI information is “1”.